



News Release

FREE MODEL FOUNDRY ANNOUNCES AVAILABILITY OF VHDL AND VERILOG MODELS FOR SPANSION'S MIRRORBIT® ORNAND™ SOLUTIONS

Addition of Spansion Flash Memory Marks 10,000th Part Number for Free Model Foundry

San Jose, Calif., August 8, 2006 (at the Flash Memory Summit) – Free Model Foundry (FMF), an open source model warehouse and design services company promoting development and distribution of simulation models of electronic components, today announced the development of VHDL and Verilog models for Spansion's MirrorBit® ORNAND™ solutions. Developed by the services division of FMF, Model Foundry Services (MFS), Spansion's Flash memory marks the 10,000th simulation part number to be added to FMF's open source warehouse.

Developed by FMF, the new VHDL and Verilog models assist designers using Spansion™ Flash devices during the design development and verification process of systems, as well as contribute to optimizing memory system performance for wireless applications.

"Spansion's MirrorBit ORNAND solutions are giving handset manufacturers the capability to design their next-generation phones with advanced digital content capabilities and provide more data storage," said Steve Schrepferman, vice president of marketing and MCP development, Wireless Solutions Division, Spansion. "The availability of simulation models, such as the recent additions from FMF, eases the development and implementation for handset designers, enabling a faster time to market for our customers."

In today's quickly changing mobile market, handset designers increasingly require Flash memory solutions that are not only reliable for data and code storage, but also be scalable, supporting the lowest tier handset up to a fully featured phone and smart phone. With the combination of Spansion MirrorBit NOR Flash memory for code execution and the MirrorBit ORNAND product for data storage, Spansion delivers a complete memory subsystem for a range of phones that enable DVD-quality video, CD-quality audio and up to 5-megapixel photos on wireless handsets.

"High-quality simulation models are critical tools for chip and system designers, especially with the growing memory requirements for mobile applications," said Richard Munden, CEO of Model Foundry Services and one of the founders of the Free Model Foundry. "Working with Spansion, we are contributing to reduced development cycle times for users and we are allowing them to focus on providing more innovative designs."

Users of Free Model Foundry can choose from two different modeling methods for each part of the family implemented, according to their needs. One method uses static memory allocation

for users whose simulations utilize a large fraction of the total capacity of large memories. The other is a dynamic allocation method that generates only the portion of the memory actually used, provided the user's simulation only accesses a relatively small portion of the part's total capacity during a specific simulation run.

The VHDL implementations developed by Model Foundry Services provide both model types as separate architectures within a single model. The Verilog implementations use standard Verilog for static allocation and SystemVerilog for dynamic allocation.

To download the Spansion Flash Memory and other open source models, visit FMF's model foundry library at <http://www.freemodelfoundry.com/>

About Free Model Foundry

Free Model Foundry's (FMF) Open Source Model Warehouse promotes widespread functional and timing simulation to system designers around the world and helps solve one of the biggest problems in chip design: finding accurate, usable component models. The company just announced its new subsidiary, Model Foundry Services (MFS). Specializing in models for board level design and verification, MFS (www.modelfoundryservices.com) provides modeling development services to IC vendors, electronic design automation (EDA) consulting groups, and product design teams. To find out more about Free Model Foundry, visit www.freemodelfoundry.com

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Editorial Contact:

Jill Jacobs
Public Relations for Free Model Foundry
+1.408.266.9753
jill@mod-marketing.com